Code: EC4T3

## II B.Tech - II Semester – Regular/Supplementary Examinations – April 2017

## ANALOG ELECTRONIC CIRCUITS (ELECTRONICS & COMMUNICATION ENGINEERING)

Duration: 3 hours Max. Marks: 70

PART - A

Answer *all* the questions. All questions carry equal marks

 $11 \times 2 = 22$ 

1.

- a) Why h-parameters are suitable to model a transistor?
- b) Compare CC amplifier with CE amplifier.
- c) Draw the high frequency CE model of transistor.
- d) Explain the significance of gain bandwidth product.
- e) Draw the small signal model of a CS amplifier.
- f) Explain the need of cascading amplifiers.
- g) State Miller's and duality of Miller's theorem.
- h) Draw the practical circuit for voltage shunt feedback amplifier.
- i) What are the general characteristics of negative feedback amplifiers?
- j) List the advantages of crystal oscillator.
- k) What is crossover distortion and how it can be eliminated?

## PART - B

Answer any *THREE* questions. All questions carry equal marks.  $3 \times 16 = 48 \text{ M}$ 

- 2. a) Compare the characteristics of transistor amplifiers in the three configurations.6 M
  - b) Consider a single stage CE amplifier with  $R_s$ =1 $K\Omega$ ,  $R_1$ =50 $K\Omega$ ,  $R_2$ =2 $K\Omega$ ,  $R_c$ =2 $K\Omega$ ,  $R_L$ =2 $K\Omega$ ,  $R_c$ =50,  $R_c$ =1.1R,  $R_o$ =25 $\mu$ A/V and  $R_o$ =2.5 $\mu$ A/V and  $R_o$ =2.5 $\mu$ A/V and  $R_o$ . Assume that  $R_o$ =1.1R0,  $R_o$ 0 are large at the operating frequency range.

10 M

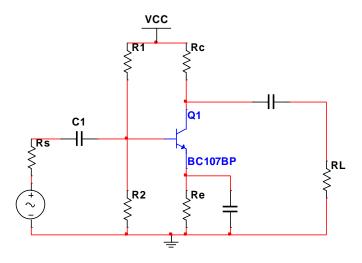


Figure 1

- 3. A transistor 's short circuit current gain is measured to be 25 at a frequency of 2 MHz. If the transistor's  $f_{\beta}$ =200 kHz Determine :
  - a) The current gain bandwidth product,  $f_T$

5 M

b) The transistor  $h_{fe}$  at low frequency.

5 M

- c) The short circuit current gain at 10 MHz and 100 MHz.

  6 M
- 4. a) Explain different coupling schemes used in multistage amplifiers with their frequency response. 8 M
  - b) Derive the expressions for A<sub>i</sub>, R<sub>i</sub>, A<sub>v</sub> and R<sub>o</sub> of bootstrapped Darlington pair.8 M
- 5. a) For the voltage series feedback amplifier, derive the expression for gain, input resistance and output resistance.

  10 M
  - b) Calculate the gain, input impedance, output impedance of voltage series feedback amplifier having A=-300,  $R_i$ =1.5K,  $R_o$ =50K and  $\beta$ =-1/20.
- 6. a) Draw and Explain the working of Hartley oscillator. 8 M
  - b) Explain transformer coupled class A amplifier and find its efficiency. 8 M