Code: EC4T3

## II B.Tech - II Semester - Regular/Supplementary Examinations -

 April 2017
## ANALOG ELECTRONIC CIRCUITS (ELECTRONICS \& COMMUNICATION ENGINEERING)

Duration: 3 hours
Max. Marks: 70
PART - A

Answer all the questions. All questions carry equal marks $11 \times 2=22$
1.
a) Why h-parameters are suitable to model a transistor?
b) Compare CC amplifier with CE amplifier.
c) Draw the high frequency CE model of transistor.
d) Explain the significance of gain bandwidth product.
e) Draw the small signal model of a CS amplifier.
f) Explain the need of cascading amplifiers.
g) State Miller's and duality of Miller's theorem.
h) Draw the practical circuit for voltage shunt feedback amplifier.
i) What are the general characteristics of negative feedback amplifiers?
j) List the advantages of crystal oscillator.
k) What is crossover distortion and how it can be eliminated?
PART - B

Answer any THREE questions. All questions carry equal marks. $3 \times 16=48 \mathrm{M}$
2. a) Compare the characteristics of transistor amplifiers in the three configurations.
b) Consider a single stage CE amplifier with $\mathrm{R}_{\mathrm{s}}=1 \mathrm{~K} \Omega$, $\mathrm{R}_{1}=50 \mathrm{~K} \Omega, \mathrm{R}_{2}=2 \mathrm{~K} \Omega, \mathrm{R}_{\mathrm{c}}=2 \mathrm{~K} \Omega, \mathrm{R}_{\mathrm{L}}=2 \mathrm{~K} \Omega, \quad \mathrm{~h}_{\mathrm{fe}}=50$, $\mathrm{h}_{\mathrm{ie}}=1.1 \mathrm{~K}, \mathrm{~h}_{\mathrm{oe}}=25 \mu \mathrm{~A} / \mathrm{V}$ and $\mathrm{h}_{\mathrm{re}}=2.5 \times 10^{-4}$ as shown in figure 1. Find $A_{i}, R_{i}, A_{v}, A_{i}, A_{v s}$ and $R_{0}$. Assume that $C_{1}$, $\mathrm{C}_{2}$ and $\mathrm{C}_{\mathrm{e}}$ are large at the operating frequency range.

10 M


Figure 1
3. A transistor 's short circuit current gain is measured to be 25 at a frequency of 2 MHz . If the transistor's $\mathrm{f}_{\beta}=200 \mathrm{kHz}$ Determine :
a) The current gain bandwidth product, $\mathrm{f}_{\mathrm{T}}$
b) The transistor $\mathrm{h}_{\mathrm{fe}}$ at low frequency.
c) The short circuit current gain at 10 MHz and 100 MHz .
4. a) Explain different coupling schemes used in multistage amplifiers with their frequency response.
b) Derive the expressions for $A_{i}, R_{i}, A_{v}$ and $R_{o}$ of bootstrapped Darlington pair.
5. a) For the voltage series feedback amplifier, derive the expression for gain, input resistance and output resistance.

10 M
b) Calculate the gain, input impedance, output impedance of voltage series feedback amplifier having $\mathrm{A}=-300, \mathrm{R}_{\mathrm{i}}=1.5 \mathrm{~K}$, $\mathrm{R}_{\mathrm{o}}=50 \mathrm{~K}$ and $\beta=-1 / 20$.
6. a) Draw and Explain the working of Hartley oscillator. 8 M
b) Explain transformer coupled class A amplifier and find its efficiency.

